



# VHDL: Analysis and Modeling of Digital Systems

*By Zainalabedin Navabi*

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## **Editorial Review**

From the Back Cover

The Definitive Guide To VHDL--Now Updated With The New VHDL93 Standard VHDL, Second Edition  
Here's the new second edition of the authoritative reference engineers need to guide them through the use of VHDL hardware description language in the analysis, simulation, and modeling of complicated microelectronics circuits. You'll find extensive new material to bring the guide fully up to date with the new VHDL93 standard, including new chapters on design flow, interfacing, modeling, and timing. Extensive appendixes, including ones on logic synthesis and CPU description style, provide up-to-date information on the use of VHDL in design. The number and depth of its relevant and practical examples and problems is what set this edition apart from other VHDL texts. Detailed coverage includes: new chapters and appendixes on design flow, various coding styles, and logic synthesis, along with detailed coverage of VHDL syntax and semantic. . .top-down design and how designers can best utilize VHDL. . .revised information on timing and concurrency. . .and VHDL applications for interface design and for special modelings, to name only some of the newly revised or newly covered topics. Plus, you'll find dozens of new examples that demonstrate or describe default binding, a sequential comparator, incremental binding, a parity check, DMA and Cache controller, use of OTHERS, aggregate operations, and use of ACCESS types. Key features of this new edition include coverage of VHDL93, timing, and modeling. The author also addresses the maturity in the use of the language that has affected the way designers use VHDL and shows how to stay a step ahead of today's applications.

About the Author

Zainalabedin Navabi, Ph.D., (Boston, MA) is adjunct professor of electrical and computer engineering at Northeastern University and the author of VHDL: Analysis and Modeling of Hardware (1993, McGraw-Hill). Since 1981, he has been involved in the design, definition, and implementation of hardware description languages synthesis and test of digital systems. He has developed and supervised the development of many HDL-related software packages and tools, and has directed student projects in VLSI design, test synthesis, simulation, synthesis, and other aspects of digital system design automation

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