Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs (Second Edition)

By Steven T. Karris

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This text includes the following chapters and appendices: Common Number Systems and Conversions, Operations in Binary, Octal, and Hexadecimal Systems, Sign Magnitude and Floating Point Arithmetic, Binary Codes, Fundamentals of Boolean Algebra, Minterms and Maxterms, Combinational Logic Circuits, Sequential Logic Circuits, Memory Devices, Advanced Arithmetic and Logic Operations, Introduction to Field Programmable Devices, Introduction to the ABEL Hardware Description Language, Introduction to VHDL, Introduction to Verilog, and Introduction to Boundary-Scan Architecture. Each chapter contains numerous practical applications. This is a design-oriented text. For additional information. please visit the Orchard Publications site.

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Review
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I must say its an excellent book, very well written and everything explained in a logical progression. I have found many books that jump in at a very fast pace making all sort of assumptions. Your book hits it just right for me. *Michael Tout, Northampton, England.* --Michael Tout, Northampton, England

About the Author
Steven T. Karris is the president and founder of Orchard Publications. His undergraduate and graduate degrees are from Christian Brothers University, Memphis, Tennessee, and from Florida Institute of Technology, Melbourne, Florida. He is a registered professional engineer in California and Florida. He has over 30 years of professional engineering experience in industry. In addition, he has over 30 years of teaching experience as an adjunct professor at several educational institutions, the most recent at UC Berkeley, CA.

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